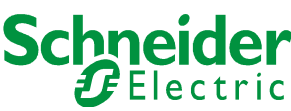


Release history for PM5340

Release Version	Date	OS Version	RS version	Language Version
1.50	14/05/2015	1.5.0	1.0.7	2.0.0
1.37	4/11/2014	1.3.7	1.0.6	2.0.0
1.12	18/03/2014	1.1.2	1.0.5	1.0.3
1.12	7/02/2014	1.1.2	1.0.3	1.0.3
1.11	7/01/2014	1.1.1	1.0.3	1.0.3

Version Number	Changes Since Version	Change Details
1.50	1.37	Enhancements <ul style="list-style-type: none"> 4 additional communication registers to represent PF value in IEC and Lag/lead format . These are 2 float 32 registers (3192, 3194) and 2 INT16 registers (3196, 3197) 10 additional logs for Firmware down load Evenly spaced pulses similar to optical pulse output implemented for Digital output Fixes <ul style="list-style-type: none"> Individual harmonic values not accessible through communication fixed Spurious neutral current while phases carry balanced currents fixed Demand Fix to Correct the behavior in Command Sync and Clock Sync Mode DI/DO/Relay counter value not reset to zero by Global reset through HMI fixed
1.37	1.12	Enhancement <ul style="list-style-type: none"> Added Float 32 energy registers to support both INT64 and Float 32 energy registers Added QR code functionality Fixes <ul style="list-style-type: none"> Fix issue: Multi tariff energy not retaining value on power cycle while Multi tariff in Input mode Fix issue: RTC day roll over not in sync with 24 hours. Fix issue: After data log roll over value only partial records are retrievable Fixed Issue : Set IP Parameters Address(1501), Set Default IP Param (1502) Earlier Not working Properly
1.12/RS1.0.5	1.12 /Rs1.0.03	Enhancement <ul style="list-style-type: none"> Support added for few internal testing
1.12/RS1.0.3	1.11	Fix : <ul style="list-style-type: none"> DLF upgrade can be done at 38400 baud rate



PM5340 FW Releases

1.11	None	Initial Ethernet release New LCD (Tianma) driver supported, 61557-12 compliance
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